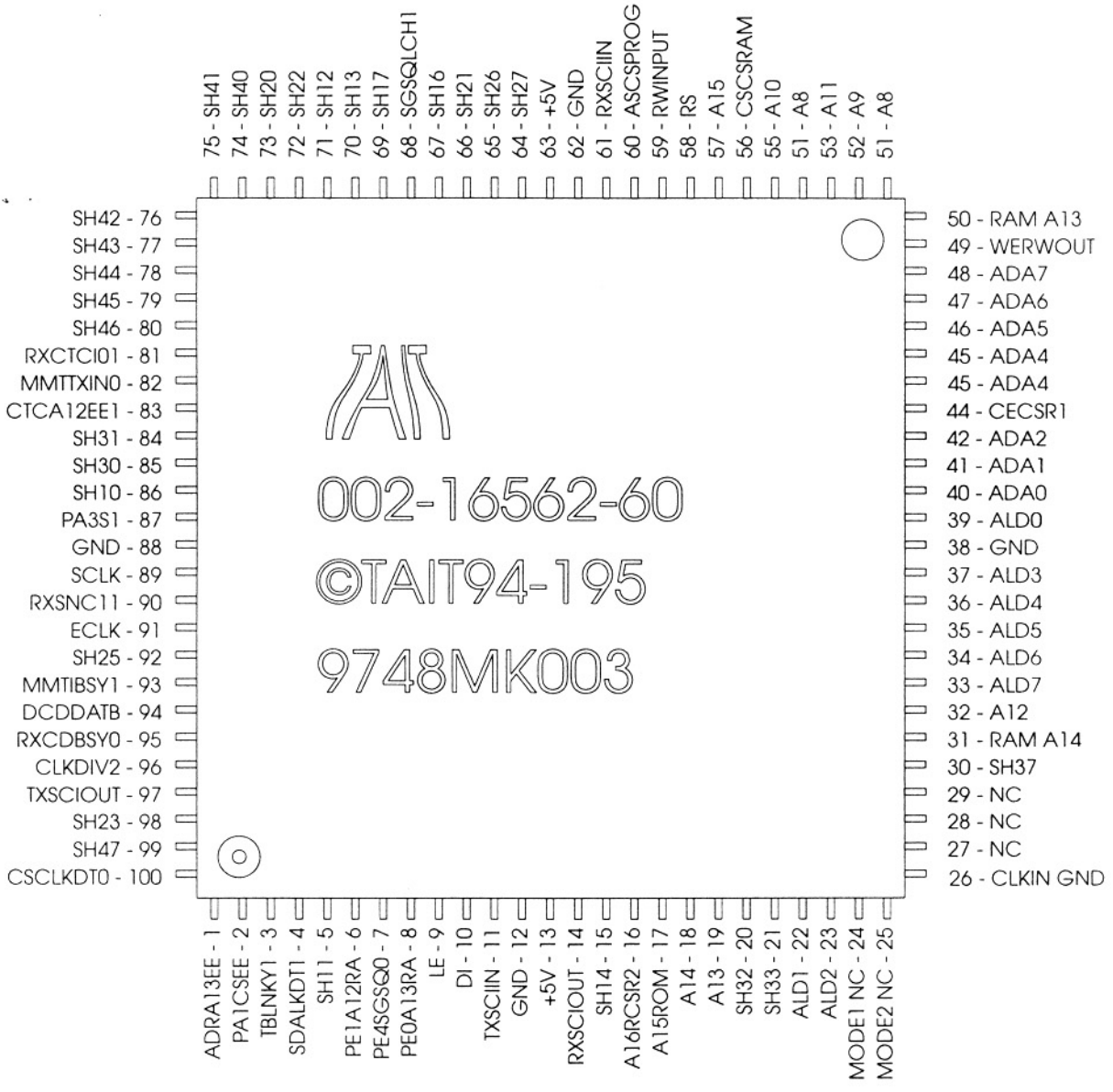


Gate Array



Power Supply etc

PIN	I/O	To/From	NAME	DESCRIPTION
13	In	+5V	+5V	+5V
63	In	+5V	+5V	+5V
12	In	GND	GND	Ground
38	In	GND	GND	Ground
62	In	GND	GND	Ground
88	In	GND	GND	Ground

Clocks

PIN	I/O	To/From	NAME	DESCRIPTION
91	In	IC505 p5	ECLK	E Clock from Micro (2MHz)
26	In	GND	CLKIN	
96	Out	IC517 p1	CLKDIV2	Clock / 2 to Modem

Control

PIN	I/O	To/From	NAME	DESCRIPTION
59	In	IC505 p6	RWINPUT	Read / #Write Input from Micro
60	In	IC505 p4	ASCSPROG	Address Strobe from Micro
58	In	IC504 p1	RS	#Reset from LVI
44	Out	IC501 p22	CECRS1	#ROM Chip Enable (also to IC502 p4)
56	Out	IC500 p20	CSCSRAM	#RAM Chip Enable (also to IC502 p5)
49	Out	IC500 p27	WERWOUT	#Write Enable (also to IC501 p31)
54	Out	IC501 p24	OEA16ROM	#Output Enable (also to IC500 p22)
1	Out	T3K44 p24	ADRA13EE	Selcall Addressing

Address

PIN	I/O	To/From	NAME	DESCRIPTION
39	Out	IC501 p12	ALD0	Decoded Address Line 0 (Also to IC500p10)
22	Out	IC501 p11	ALD1	Decoded Address Line 1 (Also to IC500p9)
23	Out	IC501 p10	ALD2	Decoded Address Line 2 (Also to IC500p8)
37	Out	IC501 p9	ALD3	Decoded Address Line 3 (Also to IC500p7)
36	Out	IC501 p8	ALD4	Decoded Address Line 4 (Also to IC500p6)
35	Out	IC501 p7	ALD5	Decoded Address Line 5 (Also to IC500p5)
34	Out	IC501 p6	ALD6	Decoded Address Line 6 (Also to IC500p4)
33	Out	IC501 p5	ALD7	Decoded Address Line 7 (Also to IC500p3)
51	Out	IC501 p27	A8	Address Line 8 (Also to IC500 p25)
52	Out	IC501 p26	A9	Address Line 9 (Also to IC500 p25)
55	Out	IC501 p23	A10	Address Line 10 (Also to IC500 p25)
53	Out	IC501 p25	A11	Address Line 11 (Also to IC500 p25)
32	Out	IC501 p4	A12	Address Line 12 (Also to IC500 p25)
19	Out	IC501 p28	A13	Address Line 13
18	Out	S17 p1	A14	Address Line 14
57	Out	S16 p12	A15	Address Line 15
17	Out	IC501 p3	A15ROM	ROM A15 Address Line
16	Out	IC501 p2	A16RCSR2	ROM A16 Address Line

Data

PIN	I/O	To/From	NAME	DESCRIPTION
40	In	IC505 p9	ADA0	Multiplexed Address/Data from Micro
41	In	IC505 p10	ADA1	Multiplexed Address/Data from Micro
42	In	IC505 p11	ADA2	Multiplexed Address/Data from Micro
43	In	IC505 p12	ADA3	Multiplexed Address/Data from Micro
44	In	IC505 p13	ADA4	Multiplexed Address/Data from Micro
45	In	IC505 p14	ADA5	Multiplexed Address/Data from Micro
46	In	IC505 p15	ADA6	Multiplexed Address/Data from Micro
47	In	IC505 p16	ADA7	Multiplexed Address/Data from Micro

Modem

PIN	I/O	To/From	NAME	DESCRIPTION
94	In	IC517 p15	DCDDATB	Carrier Detect from modem
95	In	IC517 p14	RXCDBSY0	Clocked data from modem
87	?	IC517 p7	PA3S1	Tx Data to Modem (Also to IC505 p31)
90	Out	IC517 p18	RXSNCI11	Rx Sync to modem

CTCSS

PIN	I/O	To/From	NAME	DESCRIPTION
83	Out	L501	CTCA12EE	Tx CTCSS Pulse Width Modulated
81	In	IC801 p14	RXCTCI01	Rx CTCSS Square wave

Tx/Rx Data (SCI)

PIN	I/O	To/From	NAME	DESCRIPTION
61	In	Q506	RXSCIIN	Rx Data from Control Head
14	Out	IC505 p20	RXSCIOUT	Rx Data to Micro
11	In	IC505 p21	TXSCIIN	Tx Data from Micro
97	Out	IC510 p2	TXSCIOUT	Tx Data to C/H (2020/2040 goes to R500 not IC510)